RAB 97-002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Art Unit: *2751*

Belgard, R.   )

) Examiner: *Nguyen, T*

Serial No.: *08/905,356*   )

)

Filed: *8/4/97 as continuation of parent application*)

serial no. 08/458,479 filed 6/2/95   )

)

For: *Fast Address Generator For Reducing*)

Virtual-Linear-Physical Address Conversion Time)

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AMENDMENT AND
RESPONSE TO OFFICE ACTION

Honorable Commissioner
of Patents and Trademarks
Washington, D.C. 20231

Dear Sir:

Applicant respectfully submits the following amendment and response to place this case in condition for allowance:

IN THE CLAIMS

Please amend the claims as follows:

38. (Twice amended) A system for performing address translations usable by a processor employing both segmentation and optional independent paging [said system generating an actual physical address from a virtual address in a time period T, by calculating a linear address based on said virtual address, and by calculating said actual physical address based on said calculated linear address, said system further including] the system comprising:

> means for generating an actual physical address from a virtual address in a time period T, said virtual address having both a segment identifier and a segment offset by calculating a linear address based on said entire virtual address, and by calculating said actual physical address based on said calculated linear address; and

> a fast physical address generator for generating a fast physical address related to said virtual address in a time < T.

41